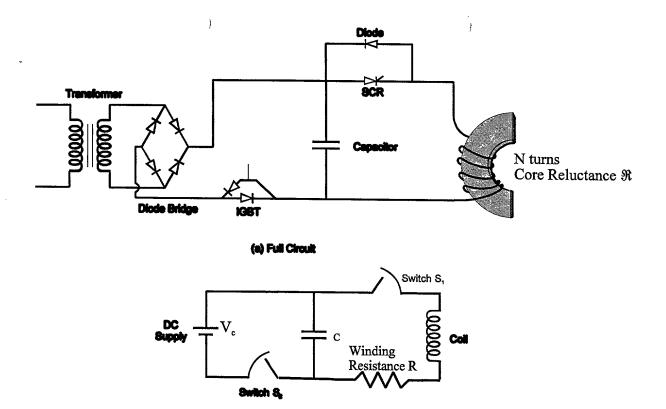
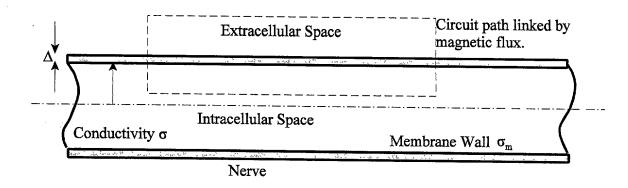


Fig. 1



(b) Simplified equivalent circuit

Fig. 2



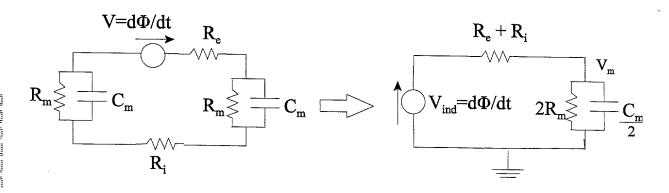
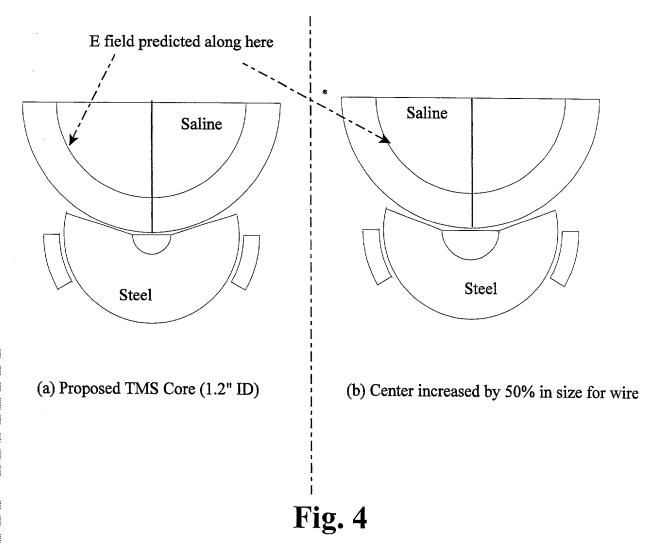


Fig. 3



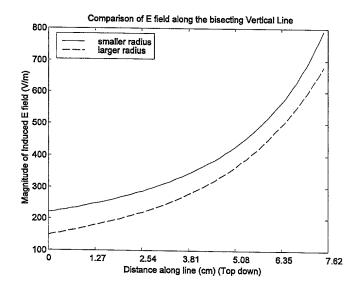


Fig. 5

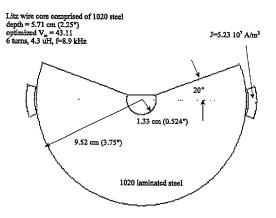
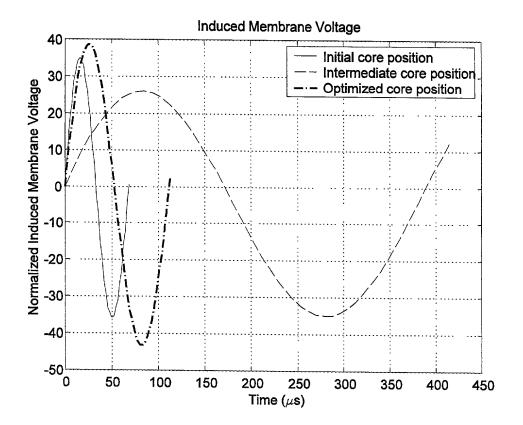


Fig. 6



**Fig.** 7